

	Hits	Search Text	DBs
1	45	reduc\$3 with (processor adj array)	US-PGPUB; USPAT
2	10	reduc\$3 with (processor adj array) with size	US-PGPUB; USPAT
3	2538	re\$1configur\$5 with processor	US-PGPUB; USPAT
4	796	re\$1configur\$5 adj3 processor	US-PGPUB; USPAT
5	119	(re\$1configur\$5 adj3 processor) with dynamic\$5	US-PGPUB; USPAT
6	214	(re\$1configur\$5 adj3 processor) with operat\$5	US-PGPUB; USPAT
7	20	((re\$1configur\$5 adj3 processor) with dynamic\$5) and ((re\$1configur\$5 adj3 processor) with operat\$5)	US-PGPUB; USPAT
8	7	(re\$1configur\$5 near3 processor) with (operat\$5 near (during or while))	US-PGPUB; USPAT
9	238	reduc\$3 with (processor near2 size)	US-PGPUB; USPAT
10	3	(reduc\$3 with (processor near2 size)) same (re\$1configur\$5)	US-PGPUB; USPAT
11	3	re\$1configur\$5 with (processor adj array) with size	US-PGPUB; USPAT
12	1	de\$1featur\$5 with dynamic\$5	US-PGPUB; USPAT
13	1	de\$1featur\$5 with (cpu or processor or micro\$1processor)	US-PGPUB; USPAT
14	17	de\$1featur\$5	US-PGPUB; USPAT
15	203	(disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)	US-PGPUB; USPAT
16	25	((disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	US-PGPUB; USPAT
17	6	((disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)) and 713/100	US-PGPUB; USPAT
18	84	(disabl\$3 near2 (portion\$1 or sub\$1system)) with (processor or cpu or micro\$1processor)	US-PGPUB; USPAT
19	3	((disabl\$3 near2 (portion\$1 or sub\$1system)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	US-PGPUB; USPAT
20	3	((disabl\$3 near2 (portion\$1 or sub\$1system)) with (processor or cpu or micro\$1processor)) and 713/100	US-PGPUB; USPAT

	Hits	Search Text	DBs
21	68	(disabl\$3 near2 (element\$1)) with (processor or cpu or micro\$lprocessor)	US-PGPUB; USPAT
22	5	((disabl\$3 near2 (element\$1)) with (processor or cpu or micro\$lprocessor)) same (re\$1configur\$5 or configur\$5)	US-PGPUB; USPAT
23	2	((disabl\$3 near2 (element\$1)) with (processor or cpu or micro\$lprocessor)) and 713/100	US-PGPUB; USPAT
24	1	(selective\$3 with (disabl\$3 near2 (processor or cpu or micro\$lprocessor))) and 713/100	US-PGPUB; USPAT
25	36	(disabl\$3 near2 (processor or cpu or micro\$lprocessor)) and 713/100	US-PGPUB; USPAT
26	544	(disabl\$3 near2 (processor or cpu or micro\$lprocessor)) same instruction\$1	US-PGPUB; USPAT
27	28	(disabl\$3 near2 (processor or cpu or micro\$lprocessor)) same (prevent\$3 with instruction\$1)	US-PGPUB; USPAT
28	54	(disabl\$3 near2 (processor or cpu or micro\$lprocessor)) same (complet\$3 with instruction\$1)	US-PGPUB; USPAT
29	0	(disabl\$3 near2 (processor or cpu or micro\$lprocessor)) same (complet\$3 with (pending or outstanding) with instruction\$1)	US-PGPUB; USPAT
30	0	(disabl\$3 near2 (module or element or function\$2)) same (complet\$3 with (pending or outstanding) with instruction\$1)	US-PGPUB; USPAT
31	227	((processor or cpu or micro\$lprocessor)) same (complet\$3 with (pending or outstanding) with instruction\$1)	US-PGPUB; USPAT
32	147	((processor or cpu or micro\$lprocessor)) with (complet\$3 with (pending or outstanding) with instruction\$1)	US-PGPUB; USPAT
33	30	((processor or cpu or micro\$lprocessor) with (complet\$3 with (pending or outstanding) with instruction\$1)) and "713"	US-PGPUB; USPAT
34	1	processor with array with fenc\$3	US-PGPUB; USPAT

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35	133	processor with fenc\$3	US-PGPUB; USPAT
36	11	(processor with fenc\$3) same (configur\$5 or re\$1configur\$5)	US-PGPUB; USPAT
37	4	reduc\$3 with (processor adj array)	EPO; JPO
38	0	de\$1featur\$5 with (cpu or processor or micro\$1processor)	EPO; JPO
39	0	((disabl\$3 near2 (feature\$1 or capabilit\$3)) with (processor or cpu or micro\$1processor)) same (re\$1configur\$5 or configur\$5)	EPO; JPO
40	8	(re\$1configur\$5 same (pending adj instructions))	US-PGPUB; USPAT
41	0	(re\$1configur\$5 same (pending adj operations))	US-PGPUB; USPAT
42	65	((re\$1configur\$5 with processor) same (pending))	US-PGPUB; USPAT
43	224	((re\$1configur\$5 with processor) same (complet\$3))	US-PGPUB; USPAT
44	14	((re\$1configur\$5 with processor) same ((complet\$3 or finish\$3) with (wait\$3 or until)))	US-PGPUB; USPAT
45	75	((re\$1configur\$5 with processor) same ((complet\$3 or finish\$3) with (instructions or operations or processes or tasks)))	US-PGPUB; USPAT
46	4	((re\$1configur\$5 with processor) same (outstanding))	US-PGPUB; USPAT
47	44	((re\$1configur\$5 with fpga) same ((complet\$3 or finish\$3) with (instructions or operations or processes or tasks)))	US-PGPUB; USPAT
48	15	((re\$1configur\$5 with fpga) same ((complet\$3 or finish\$3) with (wait\$3 or until)))	US-PGPUB; USPAT
49	1	(row with column with mask) and (re\$1configur\$5 adj processor)	US-PGPUB; USPAT
50	1	((row or column) with mask\$3) and (re\$1configur\$5 adj processor)	US-PGPUB; USPAT
51	5	(mask near2 register) and (re\$1configur\$5 adj processor)	US-PGPUB; USPAT
52	76	(mask near2 register) and (processor adj array)	US-PGPUB; USPAT
53	19	(row with column with mask) and (processor adj array)	US-PGPUB; USPAT

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54	6	((mask near2 register) same clock\$3) and (processor adj array)	US-PGPUB; USPAT
55	4881	(enabl\$3 or mask\$3 or power\$3) with mask\$3 with processor	US-PGPUB; USPAT
56	218	(enabl\$3 or mask\$3 or power\$3) with mask\$3 with processor with array	US-PGPUB; USPAT
57	153	(enabl\$3 or mask\$3 or power\$3) with select\$3 with processor with array	US-PGPUB; USPAT
58	37	(enabl\$3 or mask\$3 or power\$3 or select\$3) with (row and column) with processor with array with cell\$6	US-PGPUB; USPAT
59	15	(enabl\$3 or mask\$3 or power\$3) with mask\$3 with processor with array	EPO; JPO
60	6	(enabl\$3 or mask\$3 or power\$3) with select\$3 with processor with array	EPO; JPO
61	0	(enabl\$3 or mask\$3 or power\$3 or select\$3) with (row and column) with processor with array with cell\$6	EPO; JPO
62	1361	((memory near3 cell) with (enable adj signal))	USPAT
63	4	((memory near3 cell) with (enable adj signal)) same (power near2 sav\$4)	USPAT
64	68	((memory near3 cell) with (enable adj signal)) and (power near2 sav\$4)	USPAT
65	4	(selectively near3 (power\$3 near2 cell)) with memory	USPAT
66	5	(selectively near3 (power\$3 near2 cell)) with memory	US-PGPUB; USPAT
67	0	(selectively near3 (clock\$3 near2 cell)) with memory	US-PGPUB; USPAT
68	35	"5615162"	US-PGPUB; USPAT
69	22	"5615162" and (row same column)	US-PGPUB; USPAT
70	42	(selectively near3 (clock\$3 near2 memory))	US-PGPUB; USPAT
71	7	(selectively near3 (clock\$3 near2 memory)) and (power near2 sav\$4)	US-PGPUB; USPAT
72	7	(selectively near3 (clock\$3 near2 memory)) and (power near2 sav\$4)	US-PGPUB; USPAT
73	97	(selectively with (clock\$3 with memory)) and (power near2 sav\$4)	US-PGPUB; USPAT
74	51	((selectively near5 clock\$3) with memory) and (power near2 sav\$4)	US-PGPUB; USPAT

	Hits	Search Text	DBs
75	10	(row adj mask) with (column adj mask)	USPAT
76	14	(row adj mask) with (column adj mask)	US-PGPUB; USPAT
77	22	(row near mask\$3) with (column near mask\$3)	US-PGPUB; USPAT
78	1	(row near mask\$3) with (column near mask\$3) with (clock or power)	US-PGPUB; USPAT
79	1	(row near mask\$3) with (column near mask\$3) with (clock\$3 or power\$3)	US-PGPUB; USPAT
80	22	((row or column) near mask\$3) with (clock\$3 or power\$3)	US-PGPUB; USPAT
81	54	((row or column) near mask\$3) same (clock\$3 or power\$3)	US-PGPUB; USPAT
82	32	S81 not S80	US-PGPUB; USPAT
83	0	((row or column) near mask\$3) with (processors)	US-PGPUB; USPAT
84	9	((row or column) near mask\$3) with (processor)	US-PGPUB; USPAT
85	5	((selectively near5 clock\$3)) and (power near2 sav\$4) and simd	US-PGPUB; USPAT
86	2	((selectively near5 clock\$3)) and (power near2 sav\$4) and (processor near array)	US-PGPUB; USPAT
87	0	((selectively near5 clock\$3)) and ((row or column) near mask\$5) and (processor near array)	US-PGPUB; USPAT
88	1	((clock\$3)) same ((row or column) near mask\$5)) and (processor near array)	US-PGPUB; USPAT
89	1	((power\$3)) same ((row or column) near mask\$5)) and (processor near array)	US-PGPUB; USPAT
90	12	((power\$3)) and ((row or column) near mask\$5)) and (processor near array)	US-PGPUB; USPAT
91	1	((power\$3) same clock\$3) and ((row or column) near mask\$5)) and (processor near array)	US-PGPUB; USPAT
92	14	((row or column) near mask\$5)) and (processor near array)	US-PGPUB; USPAT
93	2	(selectively with (clock\$3 with memory)) and (power near2 sav\$4)	EPO; JPO
94	1	(row near mask\$3) with (column near mask\$3)	EPO; JPO

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95	0	((row or column) near mask\$3) same (clock\$3 or power\$3)	EPO; JPO
96	0	((power\$3)) and ((row or column) near mask\$5)) and (processor near array)	EPO; JPO